Figure 1.22 Transmission of a constant bit rate stream over a packet-switched network: (a) timing schematic; (b) FIFO buffer operation.

(a) [Diagram of transmission process]

(b) [Diagram of FIFO buffer operation]

\[ T_P = \text{packetization delay} \]
\[ T_N = \text{mean network packet transfer delay} \]
\[ T_B = \text{buffering delay at destination (to overcome worst-case jitter)} \]
\[ T_T = \text{total input-to-output delay} \]
\[ T_T = T_P + T_N + T_B \]

Jitter = variation in store-and-forward delay about the mean