40 Gb/s Optical Clock Recovery Based on an Optical Parametric Oscillator with Photonic Crystal Fiber

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Abstract: We demonstrate a 40 Gb/s optical clock recovery based on optical parametric oscillator using a highly nonlinear photonic crystal fiber as the dynamic gain medium.

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1. Introduction

Optical clock recovery which can recover clock pulses from an incoming signal without intermediate electronics is an essential part of high speed optical communications. Clock recovery is one of the key functions for subsequent signal processing, regeneration and optical time domain demultiplexing. Various techniques have been reported for optical clock recovery including injection locking mode locked lasers [1,2], optoelectronic oscillators [3,4], and optical parametric oscillators (OPO) [5] etc. Among all the optical clock recovery techniques, optical parametric oscillators based on nonlinear effect in fiber have the lowest response time which is in the femto-second range. As a result, clock recovery based on optical parametric oscillators can potentially work at very high speed. In addition, the recovered clock is wavelength tunable and can be used in wavelength conversion [6]. In this paper, we demonstrate a 40 Gb/s optical clock recovery based on optical parametric oscillator using a highly nonlinear photonic crystal fiber (PCF) as the gain medium.

2. Operating principle and Experimental Results

Figure 1 shows the experimental setup of the clock recovery circuit. A 20 m long highly nonlinear PCF provides the parametric gain in the cavity. The OPO is pumped by a fixed wavelength 10 GHz mode-locked laser which is externally modulated with 10 Gb/s pseudo random bit sequence. The output pulse width of the mode locked laser is 2.4 ps. Then the data signal is multiplexed to 40 Gb/s and is amplified by a high power erbium doped fiber amplifier (EDFA), which has a saturation power of 27 dBm. The high power return-to-zero 40 Gb/s data signal is used as the input data source for the clock recovery circuit.

The input data source are coupled into the OPO ring cavity by an add-drop multiplexer (ADM), which has a 3 nm pass band from port A to B and 3 nm block band from port E to B centered at 1559.8 nm as shown in the insets of Fig. 1. The parametric gain of the OPO is achieved by using 20 meters of highly nonlinear PCF, which has a nonlinear coefficient of 30.6 W⁻¹km⁻¹ and a zero dispersion wavelength around 1550 nm. An intracavity tunable optical bandpass filter with a 1.5 nm bandwidth is used to select the desired oscillating wavelength. A 90/10 coupler extracts 10% of the oscillating-signal power as the clock output. The remaining 90% of the signal is amplified by an EDFA to compensate for the loss in the cavity. An isolator is used for unidirectional oscillation. An optical delay line is used to adjust the cavity length of the OPO.
Figure 2 shows the data and the recovered clock using a 160-GHz optical sampling oscilloscope (OSO). The time resolution of the OSO is 800 femtoseconds. We use a $2^{11}-1$ pseudorandom binary sequence (PRBS) to demonstrate the clock recovery operation of the circuit. Figure 2(a) shows a portion of the 40 Gb/s input data pattern. Figure 2(b) shows the 40 Gb/s recovered optical clock corresponding to the PRBS input data as shown in Fig. 3(a). Figure 2(c) and 2(d) shows the input and output spectrum of the clock recovery circuit.

3. Conclusion

In this paper, we presented a 40 Gb/s clock recovery scheme based on OPO. A 20-m highly nonlinear photonic crystal fiber was used to provide the parametric gain to the OPO. Since this scheme is based on nonlinear effect in fiber, which has a response time in femtosecond range, this scheme can potentially operate in future ultrahigh speed signal processing systems.

4. References


Fig. 1. a) Schematic of the optical clock recovery circuit; b) Cut off band from port E to B; c) Pass band from port A to B.

Fig. 2. a) 40 Gb/s input data signal; b) input spectrum; c) output clock pulses; and d) output spectrum.